

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In RE Application of: )  
Peter Hazucha )  
Serial No.: 10/686,962 ) Art Unit: 2816  
Filing Date: 10/15/2003 ) Examiner: Ton, My Trang  
For: ANALOG VOLTAGE DISTRIBUTION )  
ON A DIE USING SWITCHED )  
CAPACITORS )  
\_\_\_\_\_) )  
Assistant Commissioner for Patents  
Washington, D.C. 20231  
Attn: Official Draftsperson

**SUBMISSION OF FORMAL DRAWINGS**

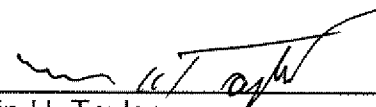
Dear Sir:

In response to the Notice of Allowability mailed 12/1/2006 enclosed herewith for filing in the above-referenced patent application are formal drawings including 7 sheets and 8 Figures. The drawing informalities have been noted and the appropriate corrections are incorporated herein.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Date: 3/1/07

  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

  
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In Chung

3/1/07  
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Date